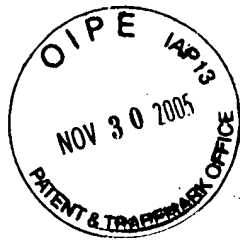


IPW

AMENDMENT TRANSMITTAL LETTER				Docket No. SON-2998	
Application No. 10/823,572-Conf. #4816	Filing Date April 14, 2004	Examiner T. Q. Phan	Art Unit 2827		
Applicant(s): Kou Nagata					
Invention: SEMICONDUCTOR MEMORY APPARATUS AND SELF-REPAIR METHOD					
TO THE COMMISSIONER FOR PATENTS Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	13	- 20 =		x	0.00
Independent Claims	1	- 3 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
<input checked="" type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ 0.00 . A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. 18-0013 as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
Ronald P. Kananen Attorney Reg. No.: 24,104 RADER, FISHMAN & GRAUER PLLC 1233 20th Street, N.W. Suite 501 Washington, DC 20036 (202) 955-3750				Dated: November 30, 2005	



Docket No.: SON-2998
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kou Nagata

Application No.: 10/823,572

Confirmation No.: 4816

Filed: April 14, 2004

Art Unit: 2827

For: SEMICONDUCTOR MEMORY APPARATUS
AND SELF-REPAIR METHOD

Examiner: T. Q. Phan

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated September 8, 2005 (Paper No./Mail Date 0805), please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.

An **Appendix** including a claim listing is attached following page 13 of this paper.